

What is claimed is:

1 A display control method for a video display system that comprises a plurality of video display apparatuses for displaying video signals being supplied as video, the display control method including:

a step in which a specific video display apparatus out of said plurality of video display apparatuses discriminates video characteristics of said video signals to output identification signals that correspond to said discriminated video characteristics and simultaneously displays said video identification signals according to display characteristics stored in correspondence to said identification signals; and

a step in which said other video display apparatuses than said specific video display apparatus display said video signals according to said display characteristics stored in correspondence to said identification signals.

2 The display control method for said video display system as claimed in claim 1, wherein said video display apparatuses including:

a step for detecting said display characteristics of said video signals;

a step for reading out said identification signals that correspond to said detected video characteristics from a memory in which said video characteristics

that is to be judgment criterion and said
identification signals have been stored
correspondingly;

5 a step for reading out said display characteristics
that correspond to said identification signals read
out from said memory in which said identification
signals and said display characteristics have been
stored correspondingly; and
10 a step for displaying said video signals according to
said read out display characteristics.

3 The display control method for said video display
system as claimed in claim 2, wherein said step for
displaying includes;

15 a step for sampling said video signals to output as
each of color signals;

a step for storing in a picture memory each of said
sampled color signals;

20 a step for producing horizontal scanning signals and
vertical scanning signals based on synchronizing
signal components of said video signals to output to
said picture memory;

a step for reading out said each of color signals, said
horizontal scanning signals, and said vertical
scanning signals to produce drive signals; and

25 a step for displaying video based on said drive signals.

4 The display control method for said video display
system as claimed in claim 3, wherein a step for
performing video adjustment for said each of color signals
is further included between said step for sampling said
5 video signals and said step for storing in said picture
memory.

5 The display control method for said video display
system as claimed in claim 1, wherein said video
characteristics are horizontal synchronizing frequencies
of said video signals.
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6 The display control method for said video display
system as claimed in claim 1, wherein said video
characteristics are vertical synchronizing frequencies of
said video signals.

7 The display control method for said video display
system as claimed in claim 1, wherein said video
characteristics are polarities of horizontal synchronizing
components of said video signals.
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8 The display control method for said video display
system as claimed in claim 1, wherein said video
characteristics are polarities of vertical synchronizing
components of said video signals.
20

9 The display control method for said video display
system as claimed in claim 1, wherein said video
characteristics are distinctions between an interlaced
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scanning and a non-interlaced scanning in said video signals.

10 The display control method for said video display system as claimed in claim 1, wherein said video
5 characteristics are frequency dividing ratios of said video signals to a synchronizing signal components.

11 The display control method for said video display system as claimed in claim 3, wherein said video
10 characteristics are writing timings of said video signals to said picture memory.

12 The display control method for said video display system as claimed in claim 3, wherein said video
15 characteristics are trapping widths of said video signals that are in a horizontal direction into said picture memory.

13 The display control method for said video display system as claimed in claim, wherein said video
characteristics are trapping widths of said video signals that are in a vertical direction into said picture memory.

20 14 The display control method for said video display system as claimed in claim 3, wherein said video characteristics are read-out timings of said video signals from said picture memory.

15 The display control method for said video display
25 system as claimed in claim 3, wherein said video

characteristics are display widths that are in a horizontal direction when said video signals are displayed.

16 The display control method for said video display system as claimed in claim 3, wherein said video
5 characteristics are display widths that are in a vertical direction when said video signals are displayed.

17 A video display system comprising a plurality of video display apparatuses for displaying video signals being supplied as video,

10 wherein a specific video display apparatus out of a plurality of the video display apparatuses includes:

a discriminator for discriminating the video characteristics of said video signals to output identification signals that correspond to the discriminated video characteristics; and

15 a controller for displaying said video signals according to the video display characteristics stored in correspondence to said identification signals, and

20 wherein said other video display apparatuses than said specific video apparatus includes a controller for displaying said video signals according to said display characteristics stored in correspondence to said identification signals.

25 18 The video display system as claimed in claim 17,

wherein each of said plurality of video display
apparatuses includes:

a detector for detecting said video characteristics of
said video signals;

5 a memory section of a video characteristic in which
said video characteristics that are to be judgment
criterion and said identification signals have been
stored correspondingly;

10 a memory section of a display characteristic in which
said identification signals and said display
characteristics have been stored correspondingly;

15 a display controller for discriminating said
identification signals that correspond to said video
characteristics detected in said detector in
reference to said memory section of a video
characteristic to read out said display
characteristics that correspond to said discriminated
identification signals in reference to said memory
section of a display characteristic; and

20 a indicator for displaying said video signals according
to said display characteristics read out in said
display controller.

19 The video display system as claimed in claim 18,
wherein said indicator includes;

25 a analog-to-digital converter for sampling said video

signals to output as each of color signals;
a picture memory for storing each of said sampled color
signals in said analog-to-digital converter;
a control circuit of a write/read timing for producing
5 horizontal scanning signals and vertical scanning
signals based on synchronizing signal components of
said video signals to output to said picture memory;
a control circuit of a display element for reading out
each of said color signals, said horizontal scanning
10 signals and said vertical scanning signals to produce
drive signals; and
a display element for displaying said video based on
said drive signals.

20 The video display system as claimed in claim 19,
15 wherein a video adjusting circuit for performing video
adjustment for each of said color signals is further
included between said analog-to-digital converter and said
picture memory.

21 The video display system as claimed in claim 17,
20 wherein said video characteristics are horizontal
synchronizing frequencies of said video signals.

22 The video display system as claimed in claim 17,
wherein said video characteristics are vertical
synchronizing frequencies of said video signals.

25 23 The video display system as claimed in claim 17,

wherein said video characteristics are polarities of horizontal synchronizing components of said video signals.

24 The video display system as claimed in claim 17,
wherein said video characteristics are polarities of
5 vertical synchronizing components of said video signals.

25 The video display system as claimed in claim 17,
wherein said video characteristics are distinctions
between an interlaced scanning and a non-interlaced
scanning of said video signals.

10 26 The video display system as claimed in claim 17,
wherein said video characteristics are frequency dividing
ratios of said video signals to synchronizing signal
components.

15 27 The video display system as claimed in claim 19,
wherein said video characteristics are writing timings of
said video signals from said picture memory.

20 28 The video display system as claimed in claim 19,
wherein said video characteristics are display widths that
are in a horizontal direction when said video signals are
displayed.

29 The video display system as claimed in claim 19,
wherein said video characteristics are trapping widths of
said video signals that are in a vertical direction into
said picture memory.

25 30 The video display system as claimed in claim 19,

wherein said video characteristics are read-out timings of said video signals from said picture memory.

31 The video display system as claimed in claim 17, wherein said video characteristics are display widths that are in a horizontal direction when said video signals are displayed.

32 The video display system as claimed in claim 17, wherein said video characteristics are display widths that are in a vertical direction when said video signals are displayed.

33 A video display system comprising a plurality of video display apparatuses for displaying video signals being supplied as video,

wherein each of said plurality of video display apparatuses includes:

a detector for detecting video characteristics of said video signals;

a memory section of a video characteristic in which said video characteristics that are to be criteria and said identification signals have been stored correspondingly;

a memory section of a display characteristic in which said identification signals and said display characteristics have been stored correspondingly;

a display controller for discriminating said

identification signals that correspond to said video characteristics detected in said detector in reference to said memory section of a video characteristic to read out said display characteristics that correspond to said detected identification signals in reference to said memory section of a display characteristic; and an indicator for displaying said video signals according to said display characteristics read out in said display controller,

wherein a specific video display apparatus out of said plurality of video display apparatuses detects said video characteristics of said video signals by said detector and read out said identification signals that correspond to said detected video characteristics from said memory section of a display characteristic to output to said other video display apparatuses and simultaneously displays said video signals according to the display characteristics stored in correspondence to said identification signals, and wherein said other apparatuses read out said display characteristics that correspond to said identification signals output from said the memory section of a display characteristic to display said

video signals according to these display characteristics.

34 The video display system as claimed in claim 33, wherein said indicator includes;

5 a analog-to-digital converter for sampling said video signals to output as each of color signals;
a picture memory for storing each of said sampled color signals in said analog-to-digital converter;
a control circuit of a write/read timing for producing
10 horizontal scanning signals and vertical scanning signals based on synchronizing signal components of said video signals to output to said picture memory;
a control circuit of a display element for reading out each of said color signals, said horizontal scanning signals and said vertical scanning signals to produce
15 drive signals; and
a display element for displaying said video based on said drive signals.

35 The video display system as claimed in claim 34,
20 wherein a video adjusting circuit for performing video adjustment for each of said color signals is further included between said analog-to-digital converter and said picture memory.

36 The video display system as claimed in claim 33,
25 wherein said video characteristics are horizontal

synchronizing frequencies of said video signals.

37 The video display system as claimed in claim 33,
wherein said video characteristics are vertical
synchronizing frequencies of said video signals.

5 38 The video display system as claimed in claim 33,
wherein said video characteristics are polarities of
horizontal synchronizing components of said video signals.

39 The video display system as claimed in claim 33,
wherein said video characteristics are polarities of
10 vertical synchronizing components of said video signals.

40 The video display system as claimed in claim 33,
wherein said video characteristics are distinctions
between an interlaced scanning and a non-interlaced
scanning of said video signals.

15 41 The video display system as claimed in claim 33,
wherein said video characteristics are frequency dividing
ratios of said video signals to synchronizing signal
components.

42 The video display system as claimed in claim 33,
20 wherein said video characteristics are writing timings of
said video signals from said picture memory.

43 The video display system as claimed in claim 33,
wherein said video characteristics are display widths that
are in a horizontal direction when said video signals are
25 displayed.

44 The video display system as claimed in claim 33,
wherein said video characteristics are trapping widths of
said video signals that are in a vertical direction into
said picture memory.

5 45 The video display system as claimed in claim 33,
wherein said video characteristics are read-out timings
of said video signals from said picture memory.

10 46 The video display system as claimed in claim 33,
wherein said video characteristics are display widths that
are in a horizontal direction when said video signals are
displayed.

15 47 The video display system as claimed in claim 33,
wherein said video characteristics are display widths that
are in a vertical direction when said video signals are
displayed.